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(72) Inventors; and

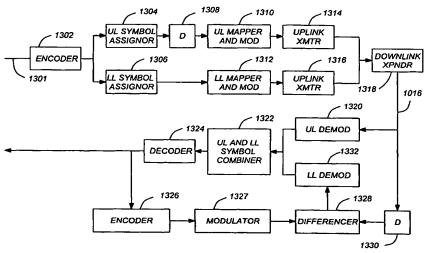
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[Continued on next page]

(54) Title: LOWER COMPLEXITY LAYERED MODULATION SIGNAL PROCESSOR



(57) Abstract: A method and apparatus for transmitting and receiving a coded signal having an upper layer signal (130) and a lower layer signal (1312) is disclosed. The method comprises the steps of combining the upper layer signal and the lower layer signal, encoding the combined upper layer signal and lower layer signal, delaying the upper layer signal, modulating the delayed upper layer signal and transmitting the lower layer signal. The apparatus comprises an encoder (1301), for encoding a combined upper layer signal and lower layer signal, a delay communicatively coupled to the encoder, for delaying the upper layer signal, a first modulator, for modulating the upper layer signal, a second modulator, for modulating the lower layer signal, a transmitter (1314), communicatively coupled to the first modulator, for transmitting the delayed upper layer signal, and a second transmitter (13, 16), communicatively coupled to the second modulator.

7O 2004/040403 A3



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C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category *	Citation of document, with indication, where ap	monriate, of the relevant nassages	Relevant to claim No.
Y	US 6,075,808 A (Tsujimoto) 13 June 2000 see figs.		
Í	os 6,075,808 A (1 sujumoto) 13 June 2000 see 11gs. and col.5, lines 50-67	1a-10, 3a-30, 4 and col.4, thes 2-27	1-03
Y	US 6,404,819 B1 (Gehlot) 11 June 2002 See figs. 2a-2b and col.5, lines 20-67 and col.6,		
•	lines 1-45		
Y	US 6,433,835 B1 (Hartson et al) 13 August 2002 See abstract and figs. 2, 4a, 5a, 6a 1-63		
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